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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In 1995 application of:

Application : Alex Kalnitsky, et al

Docket No : 93-C-32

Serial No : 08/163,043

Group : 1109

Filed : December 6, 1993

Examiner : T. Dang

For : Enhanced Planarization Technique for an Integrated Circuit

TRANSMITTAL LETTER

Hon. Commissioner of Patents and Trademarks
Washington, D.C.

Dear Sir:

Enclosed herewith is a Supplemental Declaration for filing in the above-identified application.

Please charge any fees necessary to deposit account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted,


Lisa K. Jorgenson
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Attorney for Applicant

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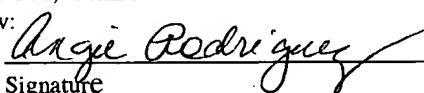
CERTIFICATE OF MAILING

37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Box Issue Fee, Commissioner of Patents and Trademarks, Washington, D.C. 20231 on the date below:

December 7, 1995

Date


Signature